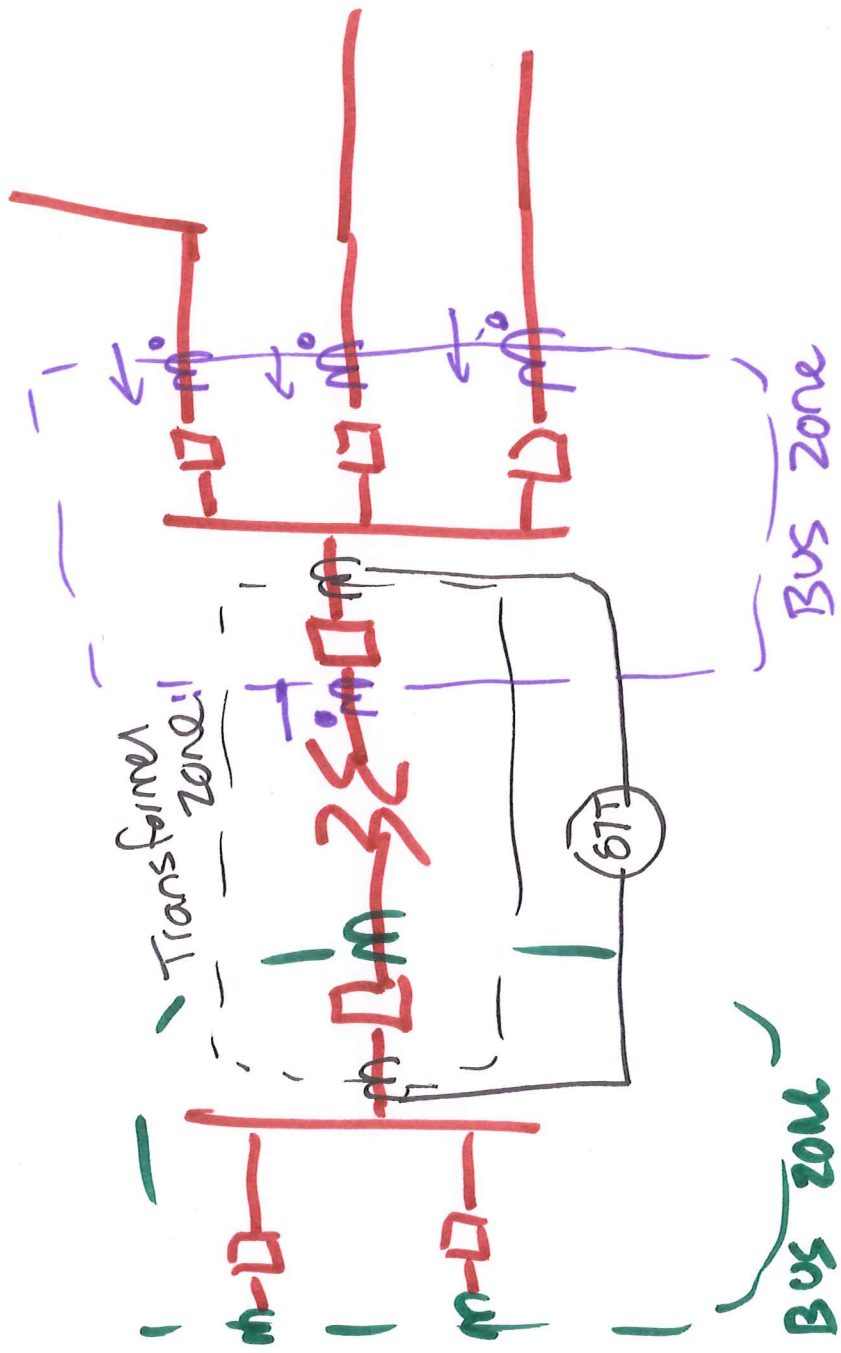


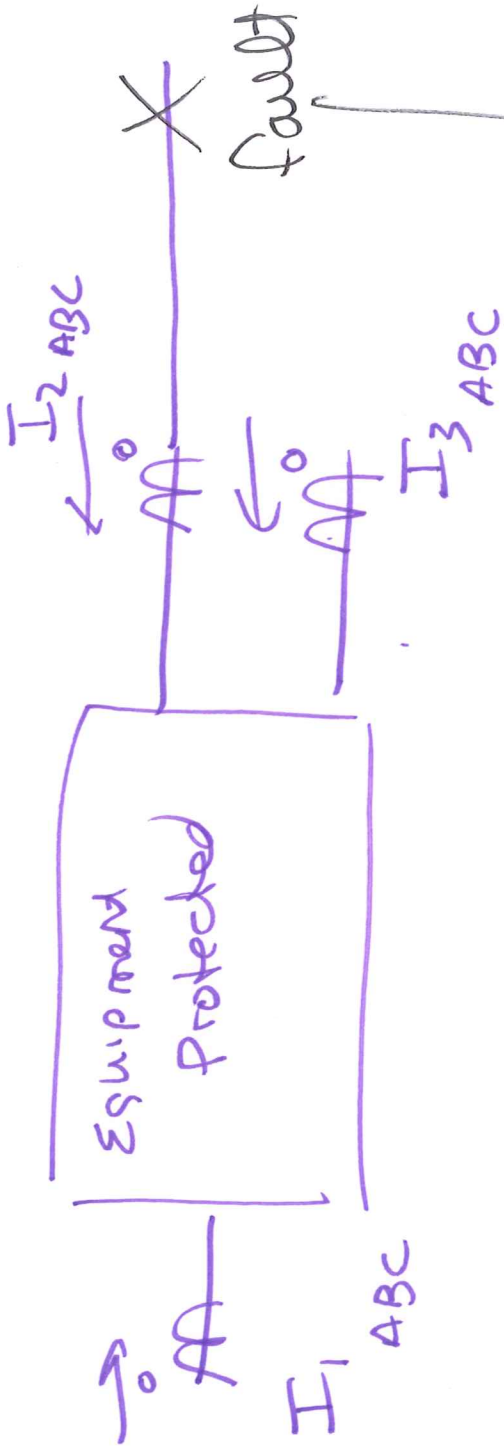
ECE 525

POWER SYSTEM PROTECTION
AND RELAYING

SESSION no. 24



- Simplest scheme - Bus Differential 87B



Normal Load conditions

$$\bar{I}_{1A} + \bar{I}_{2A} + \bar{I}_{3A} = 0$$

$$\bar{I}_{1B} + \bar{I}_{2B} + \bar{I}_{3B} = 0$$

$$\bar{I}_{1C} + \bar{I}_{2C} + \bar{I}_{3C} = 0$$

also holds for external faults

Internal fault

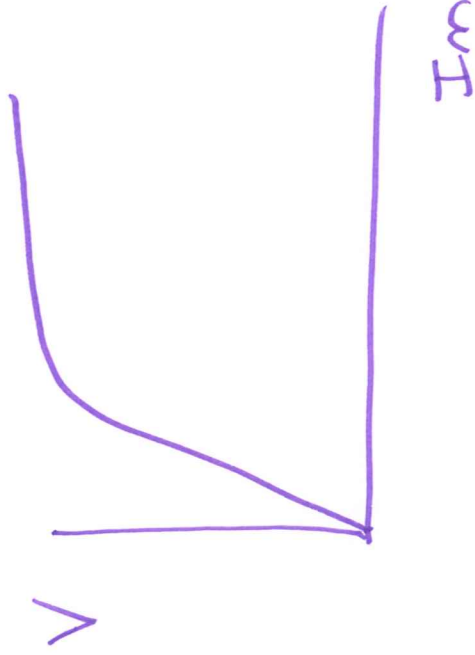
- Between the CTS

$$\bar{I}_{1A} + \bar{I}_{2A} + \bar{I}_{3A} = \bar{I}_{AF}$$

- setting a threshold to differentiate a fault from external fault or load
↑
(internal)

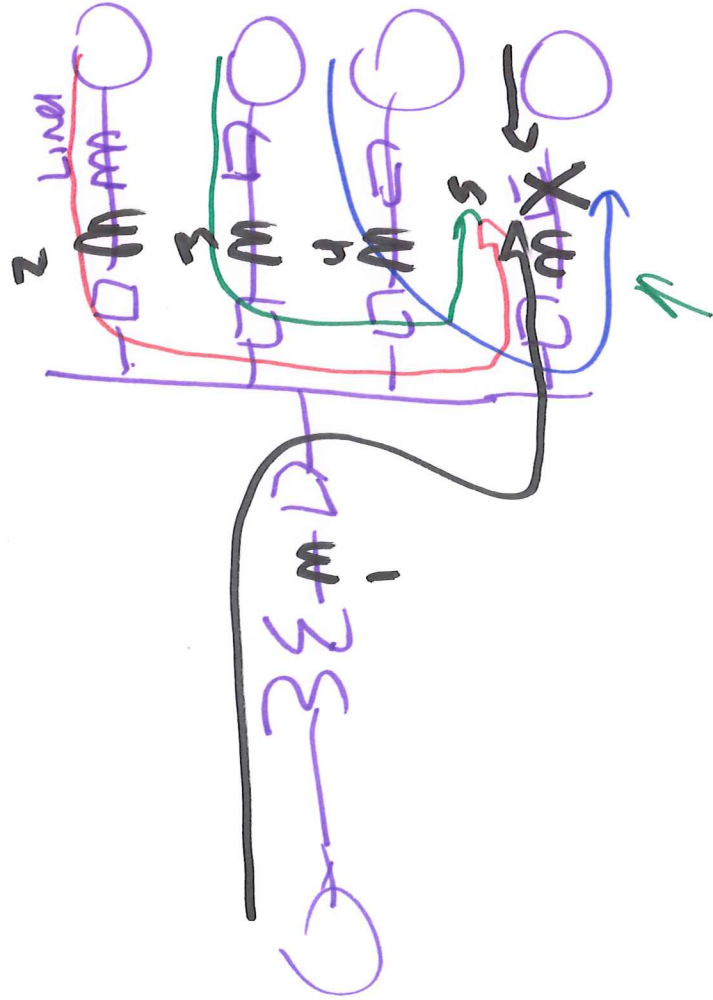
→ Relay operation on secondary current

- For bus scheme
- matched CTs
- ~~all~~ all same C-class



- CT saturation concerns

- Big concern is external faults



$$\bar{I}_{1A} + \bar{I}_{2A} + \bar{I}_{3A} + \bar{I}_{4A} + 0.5 \cdot \bar{I}_{3A} e^{j\theta} \neq 0$$

due to saturation

CT sees fault current contribution from 4 sources

Possible fixes

① Linear ~~to~~ CTs (no iron core)

- Linear couplers
- Optical CTs

② Restrained Differential Element



element restraint cal
-M-
-mm

Low impedance
impedant
differential element

→ will discuss microprocessor implementation next

③ High impedance differential

→ you know the CT on external faulted circuit will saturate

- force it ~~to~~ saturate deeply & use that in detection scheme

⇒ voltage based detection

Low impedance Bus differential

$$I_{opA} = | \bar{I}_{1A} + \bar{I}_{2A} + \bar{I}_{3A} + \dots + \bar{I}_{nA} |$$

$$I_{RTA} = | I_{1A} | + | I_{2A} | + | I_{3A} | + \dots + | I_{nA} |$$

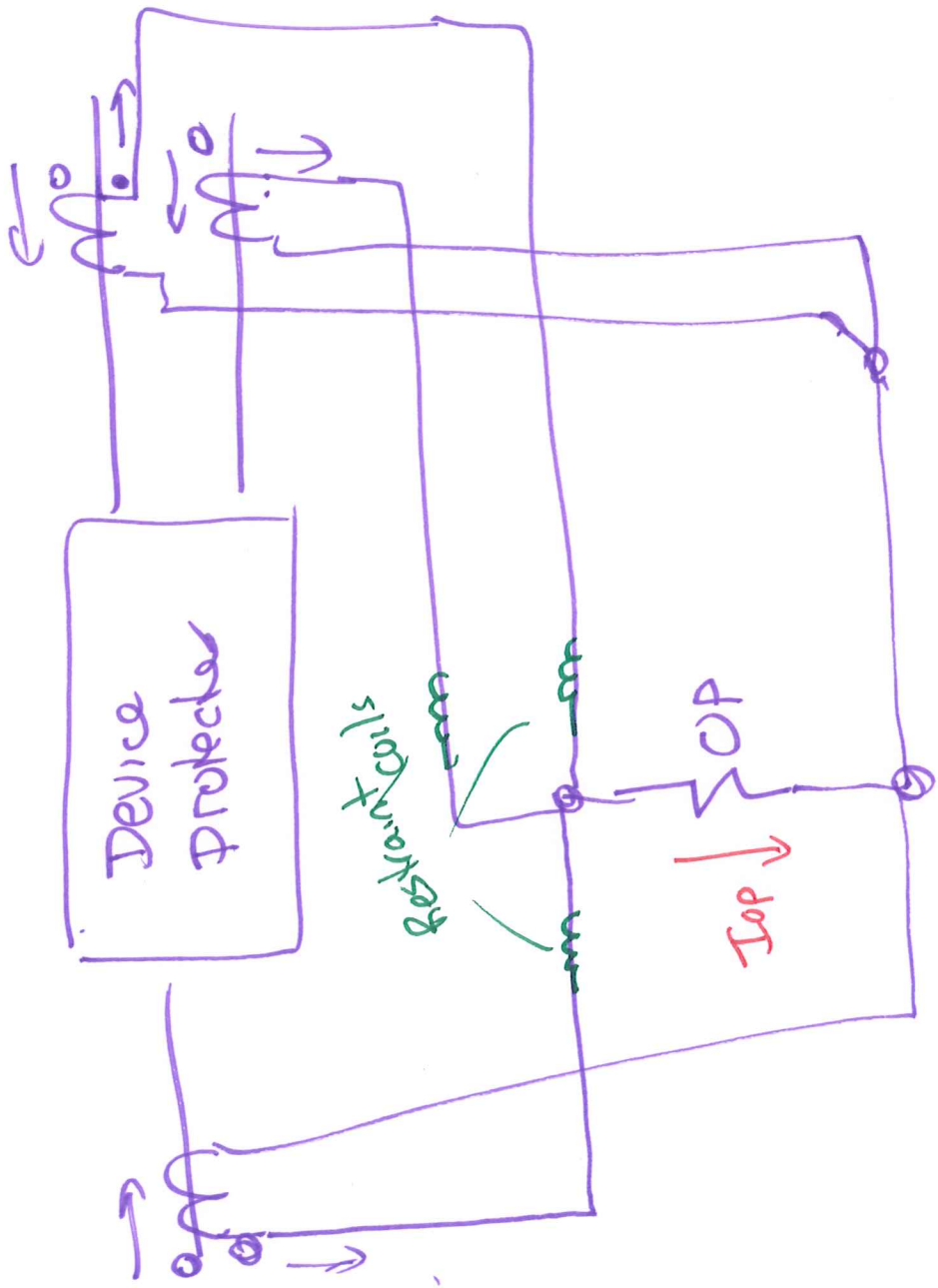
→ for load or external fault (not saturation)

$I_{opA} \rightarrow \text{small}$

$I_{RTA} \rightarrow \text{large}$

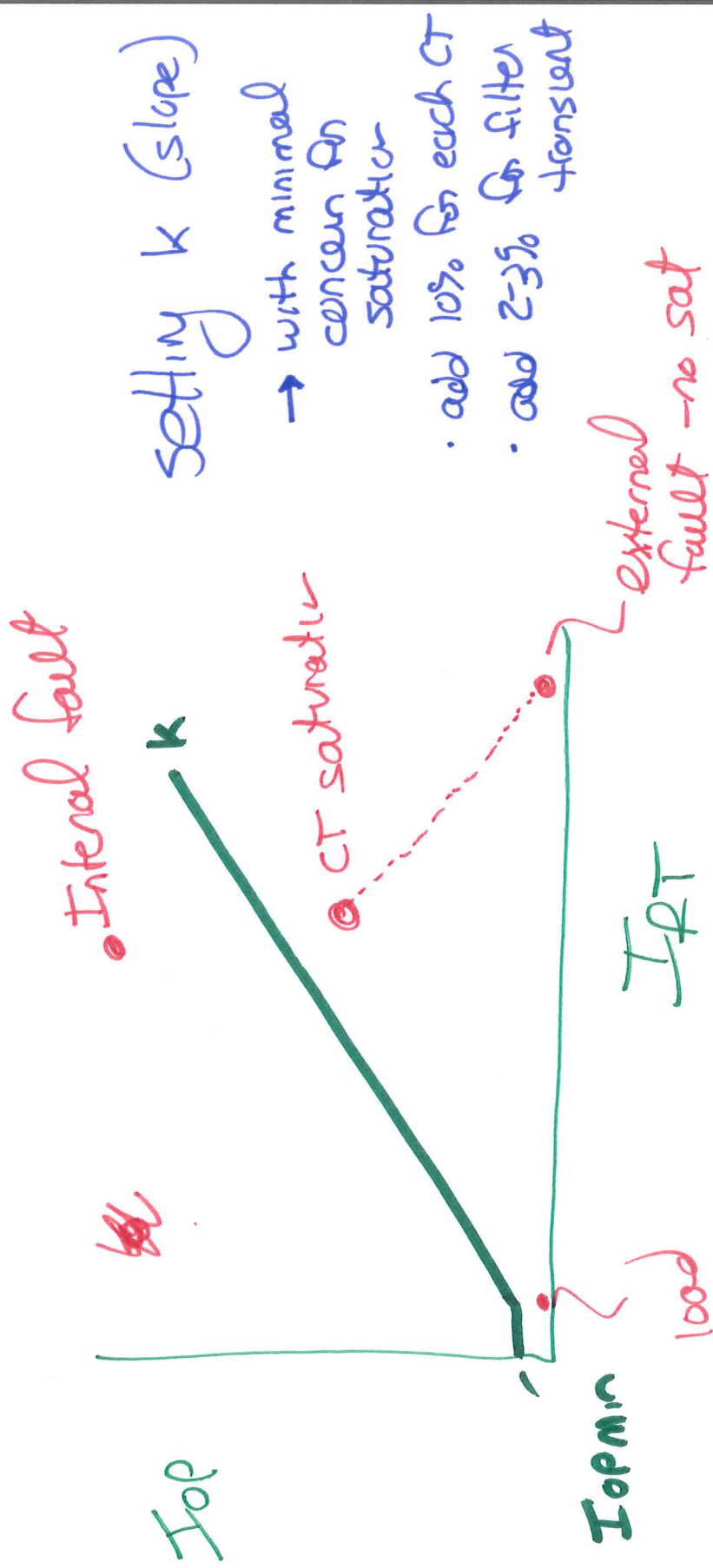
Internal fault I_{opA} large

I_{RTA} large



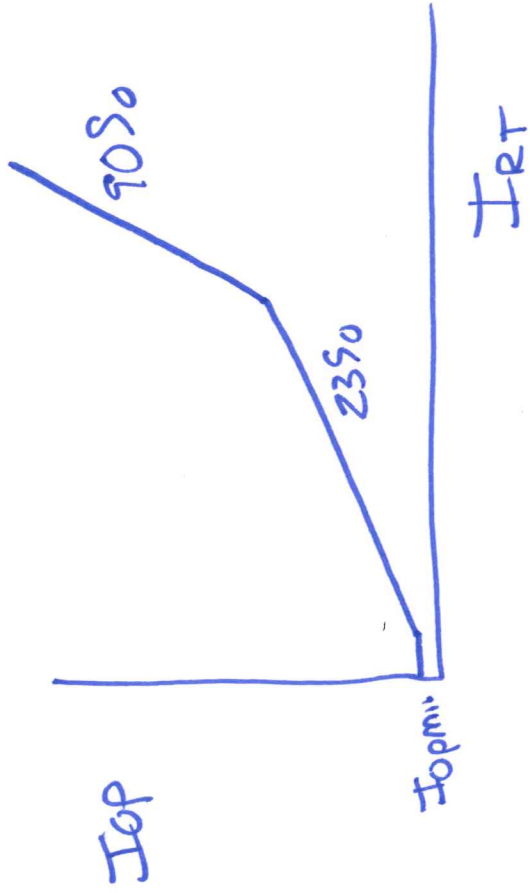
TRIP if $I_{op} > k \cdot I_{RT}$

$$k = |I_{1A}| + k \cdot |I_{2A}| + k \cdot |I_{3A}|$$



How do you handle CT saturation

(1) Dual slope characteristic



steeper slope
for big currents

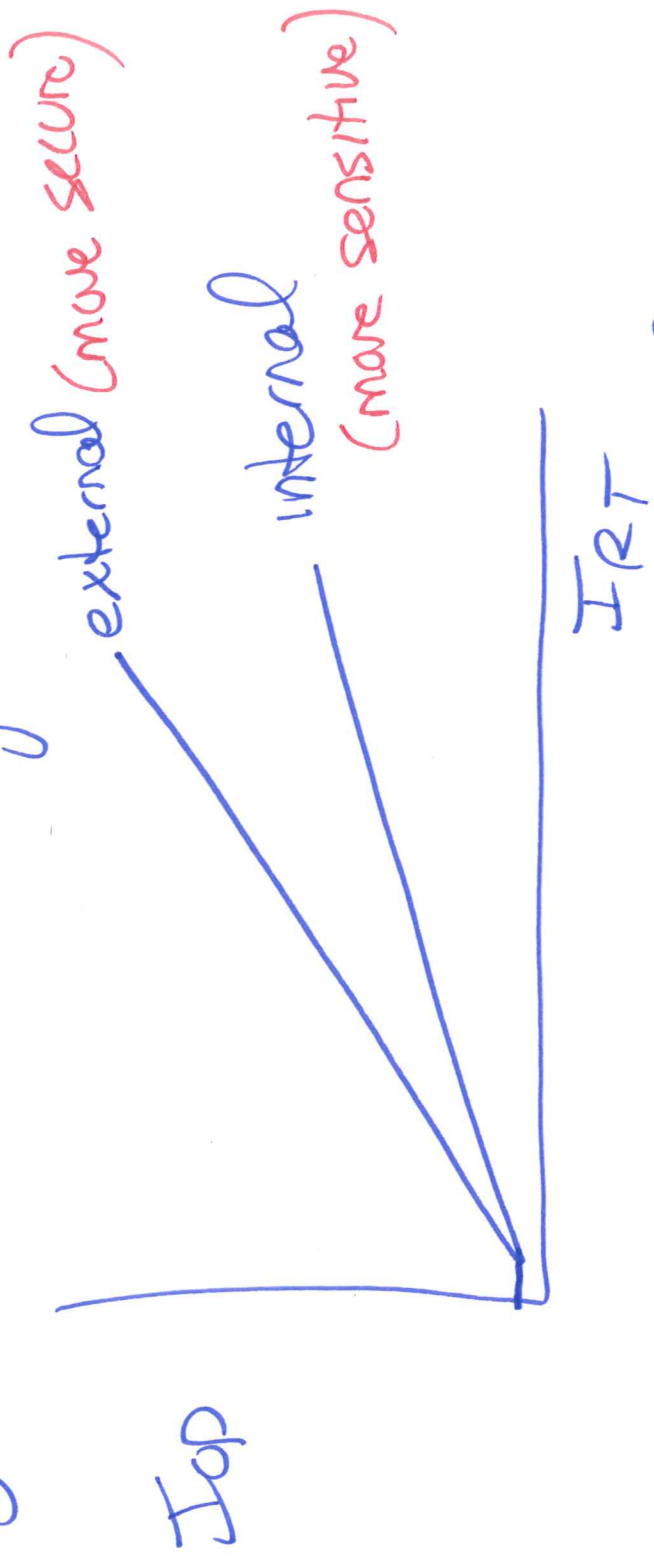
(not great or
DC offset)

2 saturates

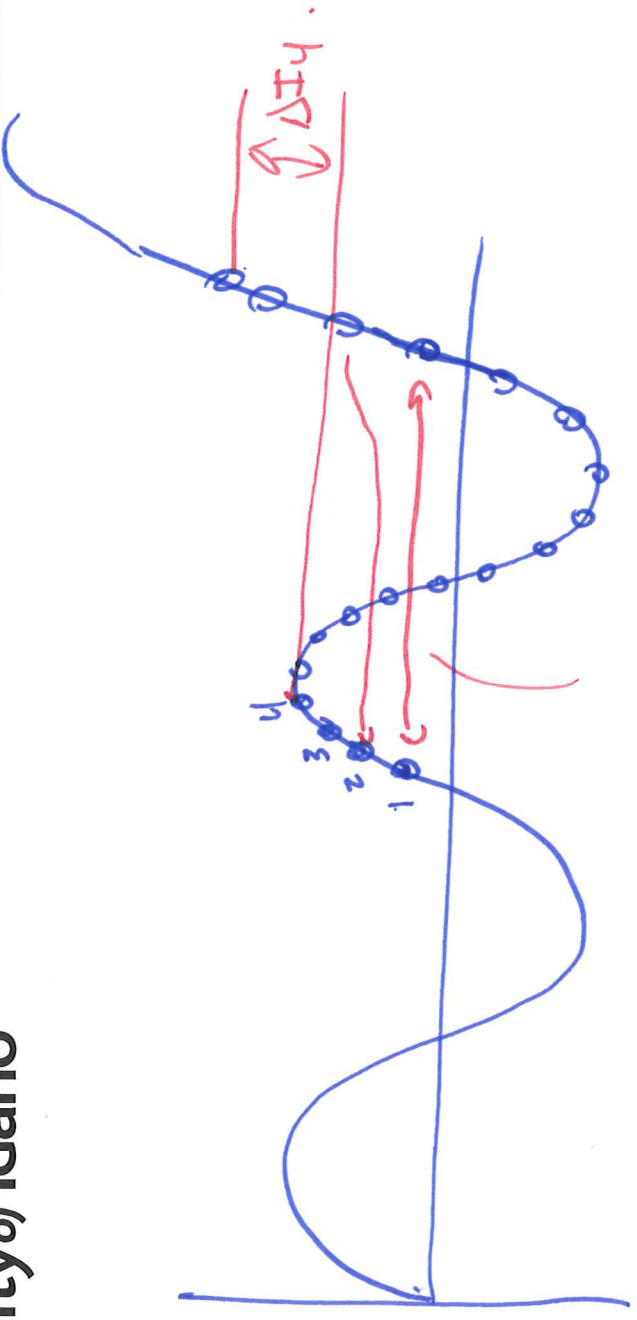
more slowly

- other protection
should act
first

② Dynamic slope setting



- Make a fast determination if fault is internal or external
 - Before saturates
 - Use unfiltered quantities (EAW) → superimposed quantities



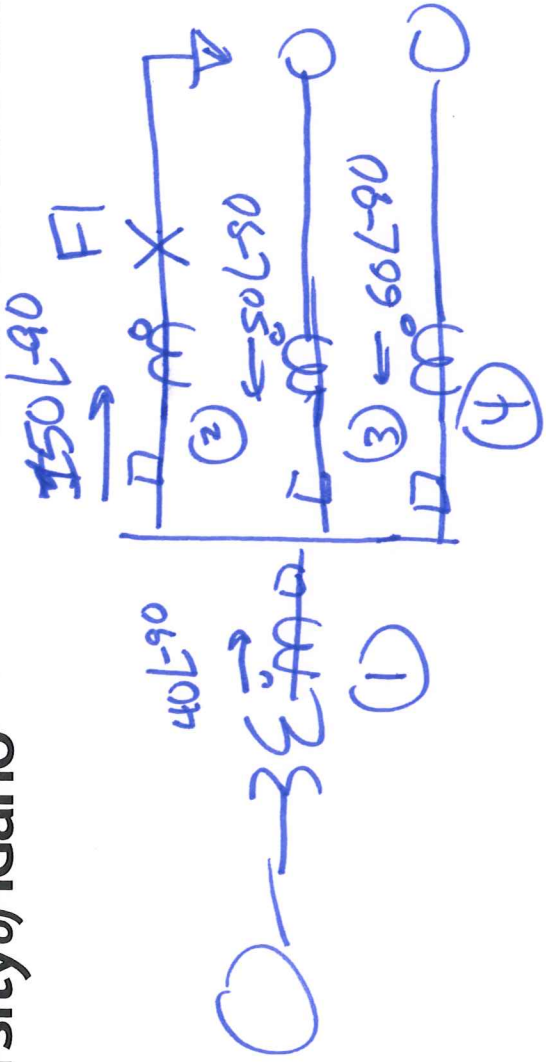
- Produce operate
current based on
these delta
quantities

- $\Delta I_{top} > \text{Threshold}$
declare
external

$$\Delta I_k = I_k - I_{k-RS}$$

cycle
old

$\Delta I_1 \approx 0$



No saturation

$$I_{op} = | \bar{I}_1 + \bar{I}_2 + \bar{I}_3 + \bar{I}_4 |$$

$$= | 40 \angle -50^\circ + 150 \angle -90^\circ + 50 \angle -90^\circ + 60 \angle -90^\circ |$$

$$= 0 \text{ A}$$

$$I_{RT} = 300 \text{ A}$$

Fault current causes CT2 to saturate
 → relay sees $\frac{2}{3}$ of current (no phase shift)

$$I_{op} = |40 \angle -90^\circ + 100 \angle 150^\circ + 50 \angle 90^\circ + 60 \angle 90^\circ|$$

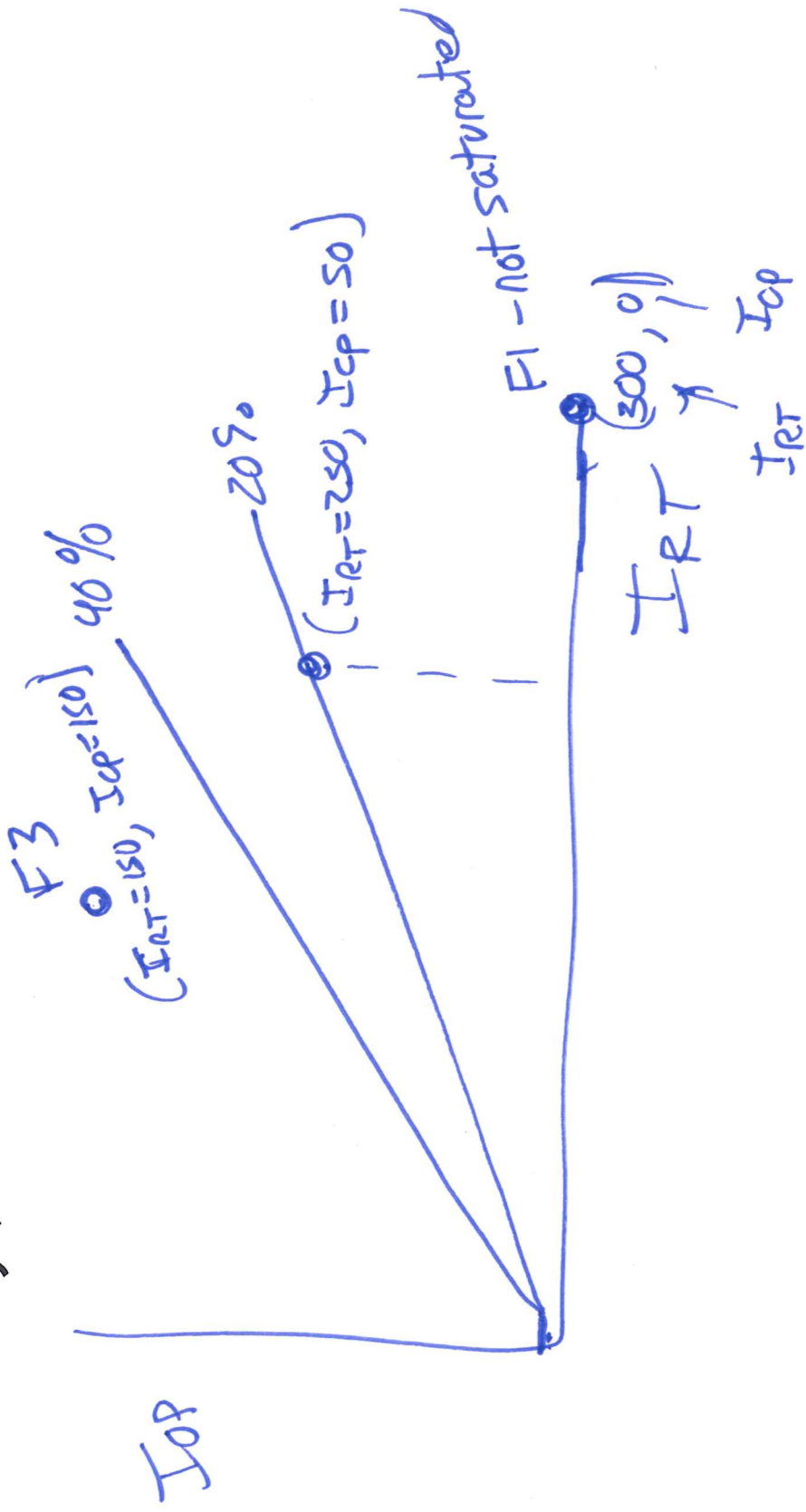
\neq

$$= |50 \angle -90^\circ| = 50$$

$$I_{RT} = 250$$

$$\frac{I_{op}}{I_{RT}} = 0.2 \quad (20\%)$$

- would trip if
 slope $< 20\%$



Internal fault



$$I_{op} = |40 \angle 90^\circ + 0 + 50 \angle -50^\circ + 60 \angle -90^\circ|$$

$$= 150A$$

$$I_{PT} = 150$$

