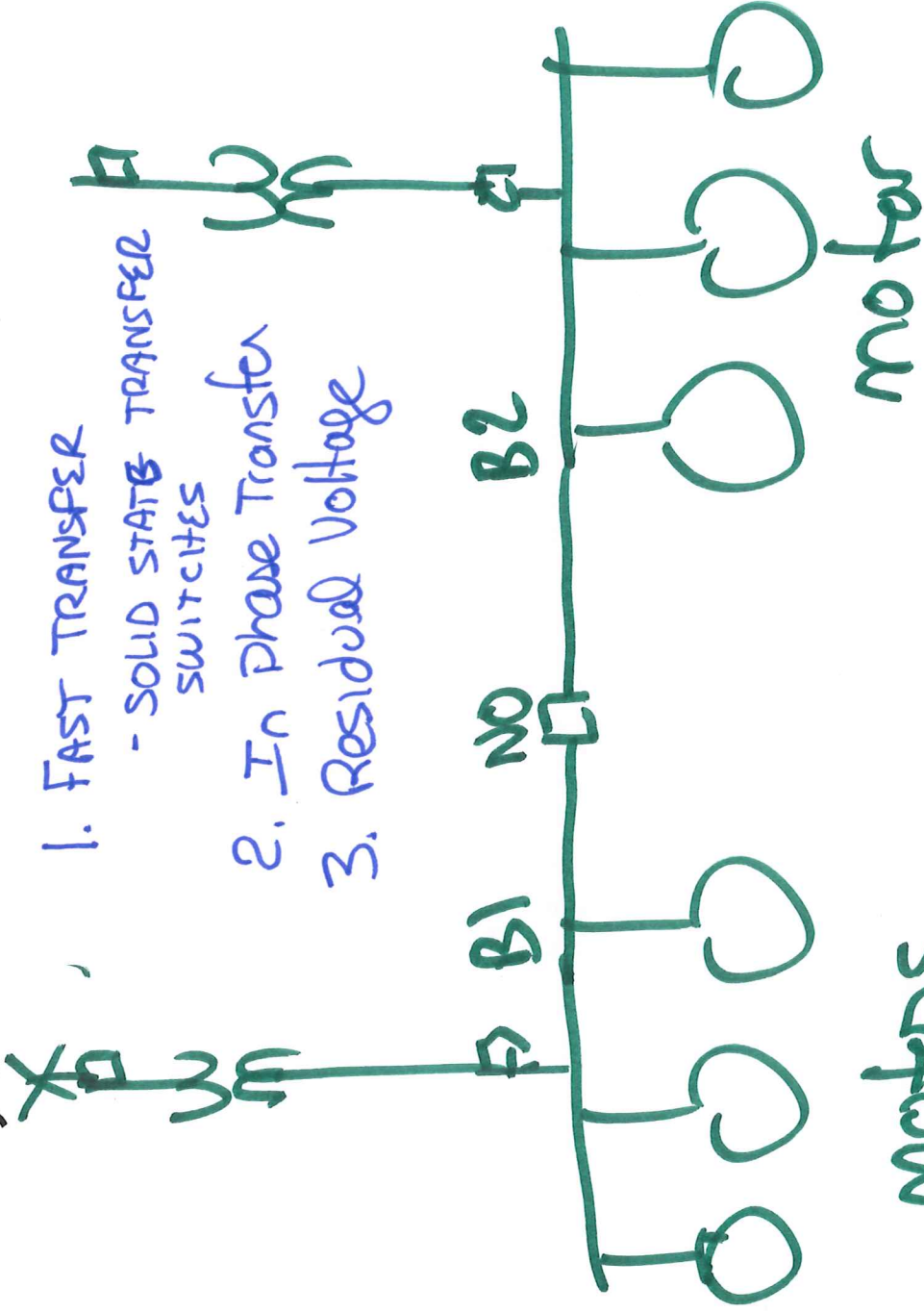


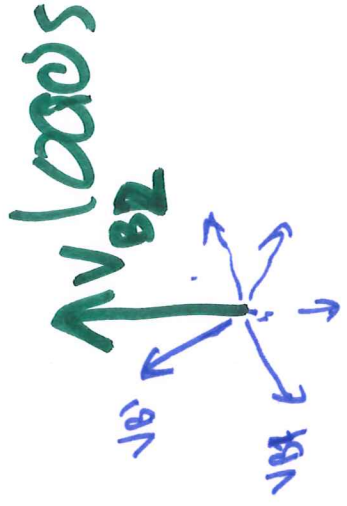
ECE 525

POWER SYSTEM PROTECTION  
AND RELAYING

SESSION no. 25



1. FAST TRANSFER  
- SOLID STATE TRANSFER SWITCHES
2. In Phase Transfer
3. Residual Voltage

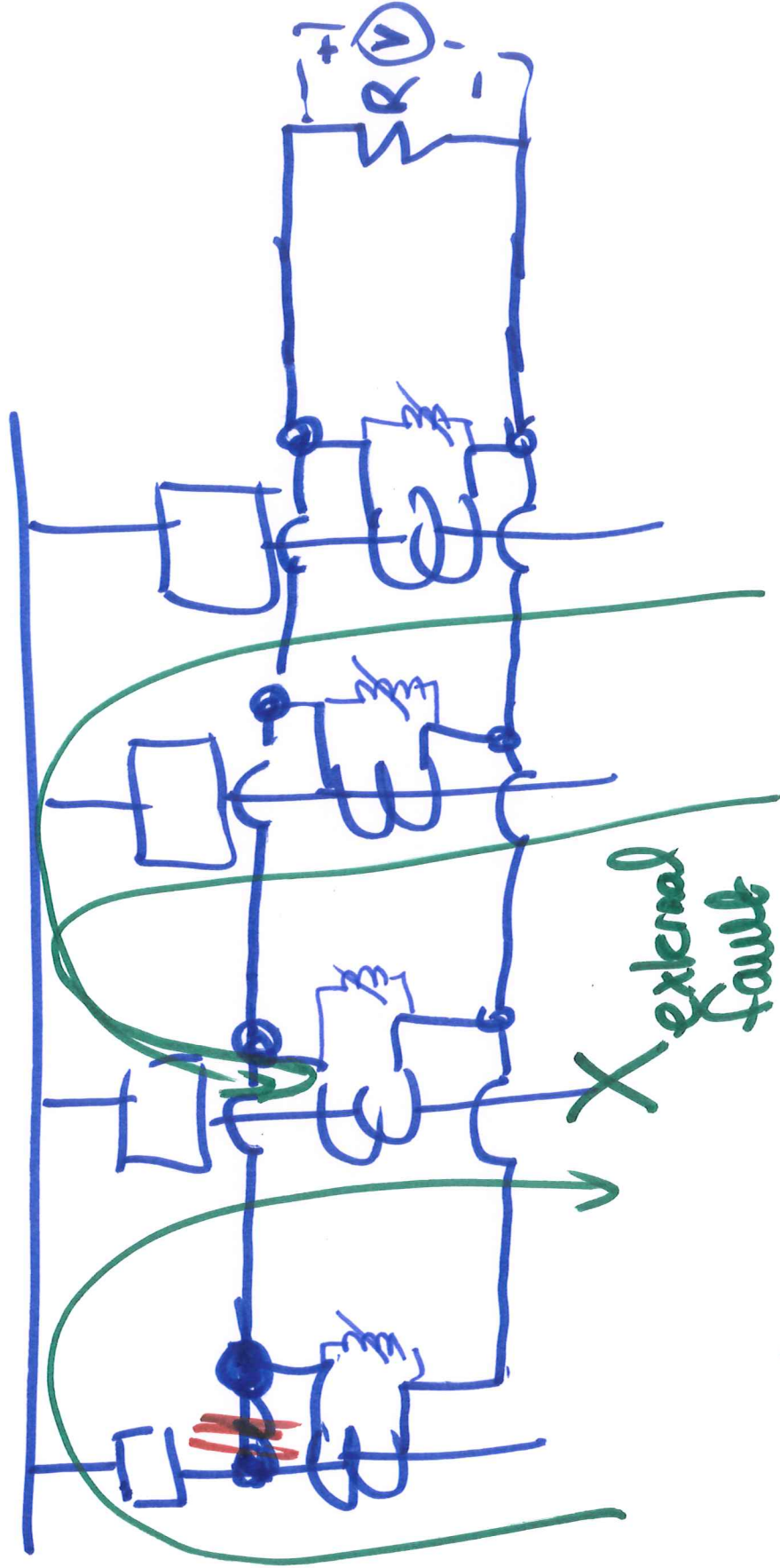


## Bus Protection

- CT saturation for differential elements
  - External
    - Trip when should not.
      - false trip
      - much bigger impact on system
  - Restrained ~~at~~ Differential
  - High Impedance Differential

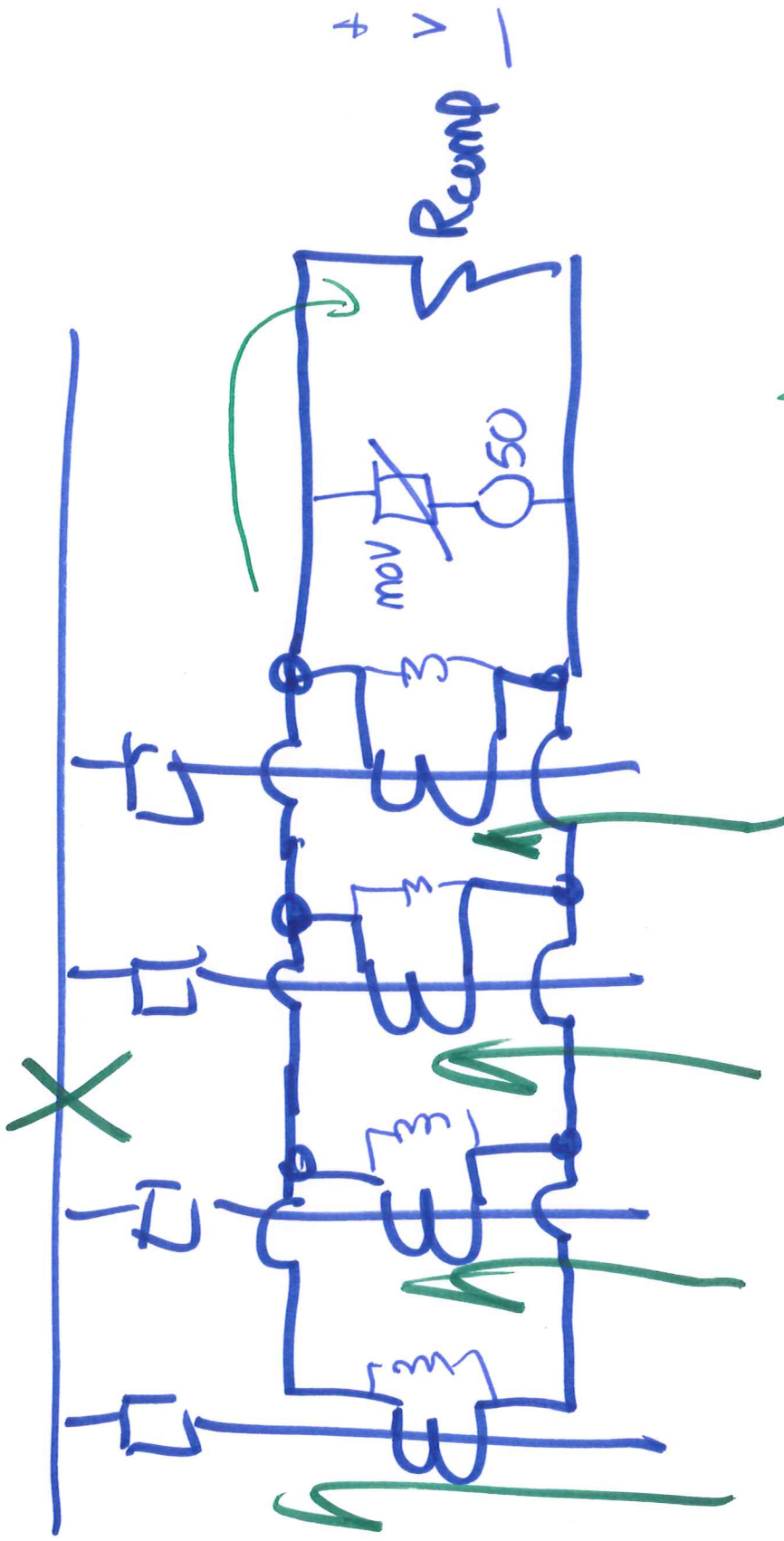
## High Impedance Differential

- ① Expect a CT to saturate for external fault
- ② I<sub>E</sub> it saturates, drive it deep into saturation



- mismatched CTs

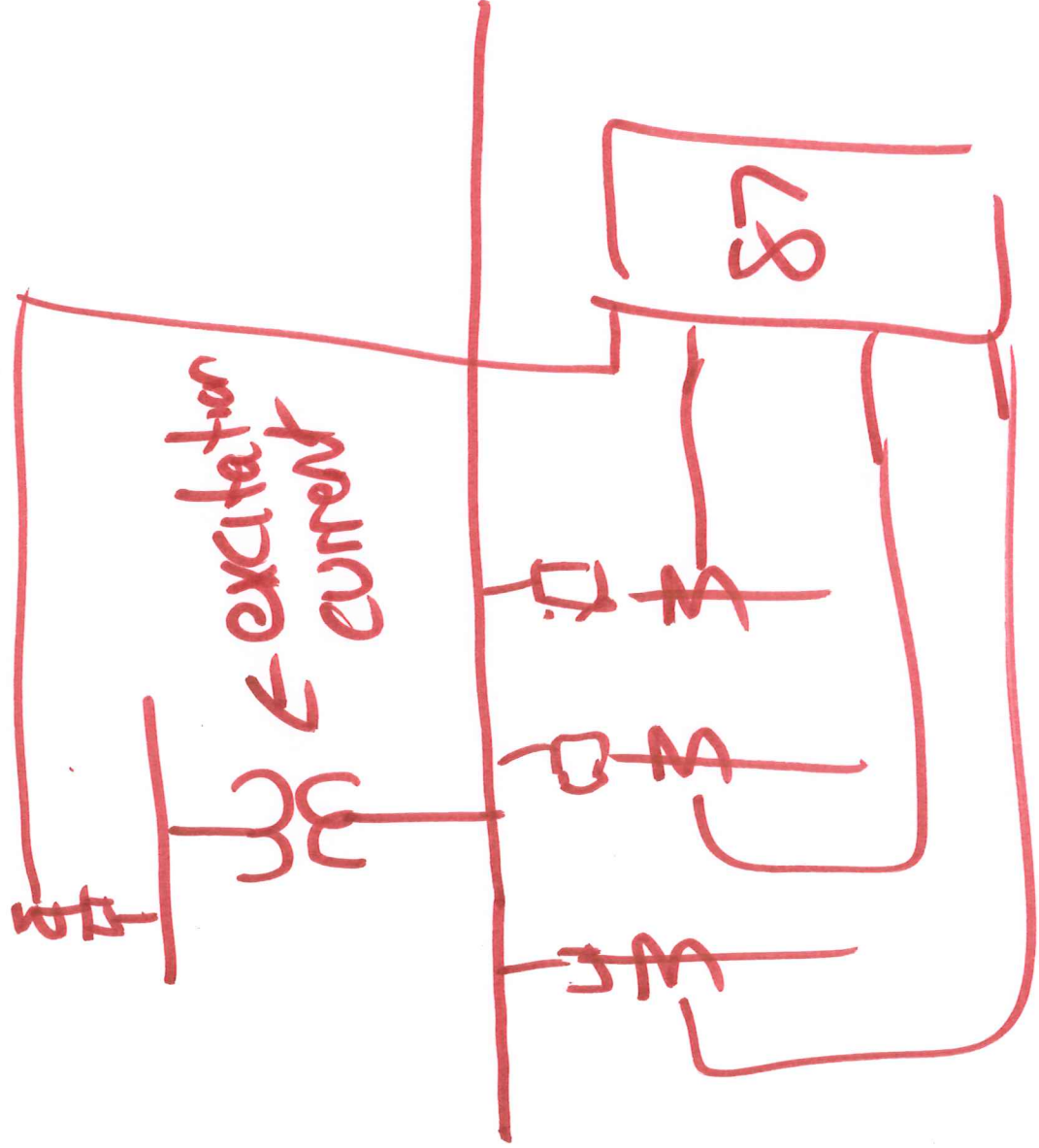
- once CT starts to saturate, drive all current mismatch into its saturation branch - due to  $R_{comp}$



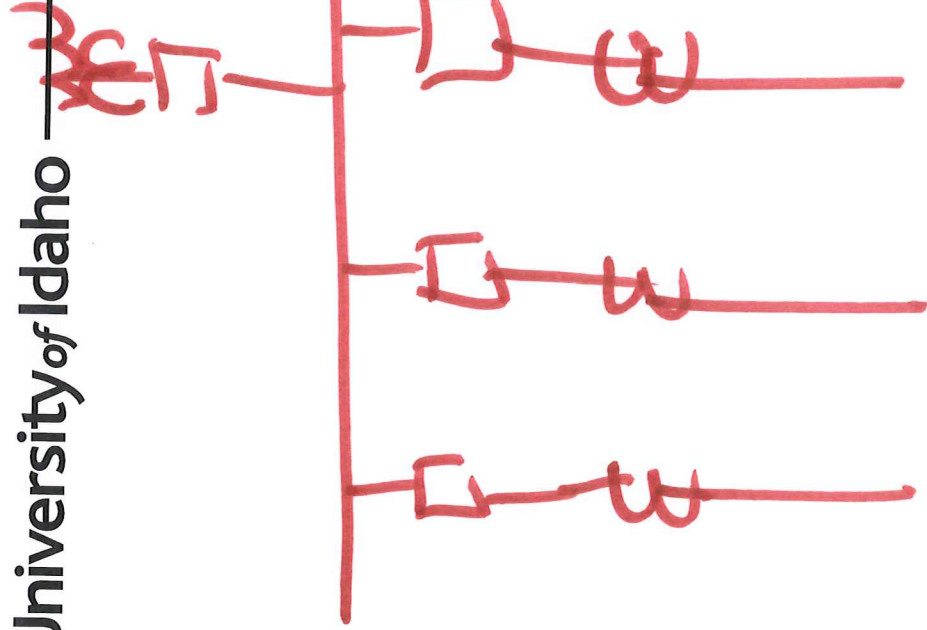
- as seen as voltage security  
above threshold  $\rightarrow$  trip

- Other challenges for bus protection
  - variable configurations
  - correctly accounting for all possibilities
  - CT wiring error
- current path without measurement in the zone

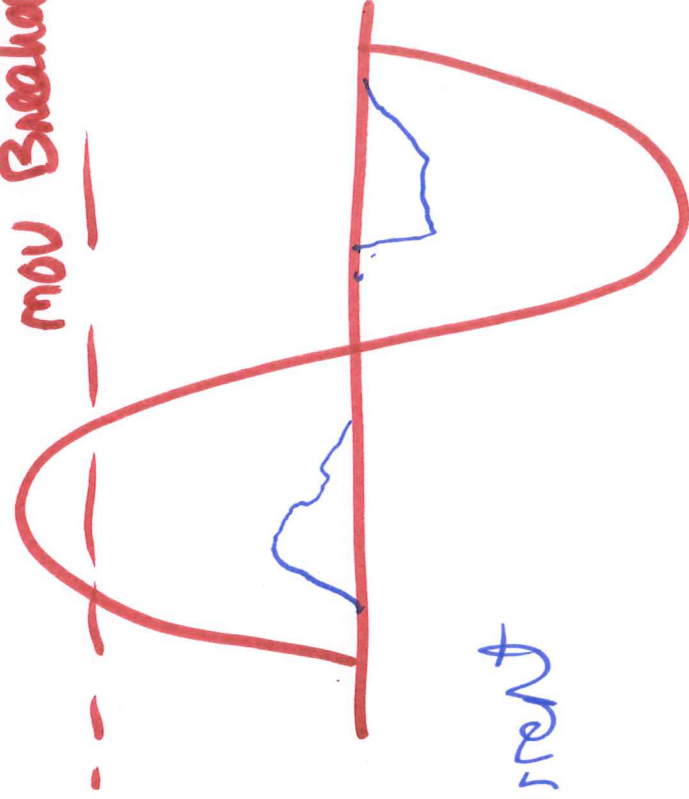
# Transformer in Bus Zone





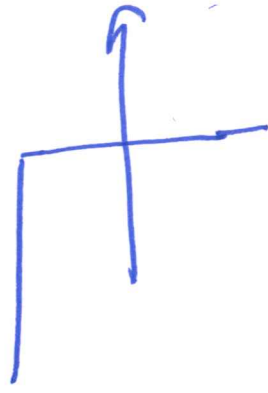


mov Breakover

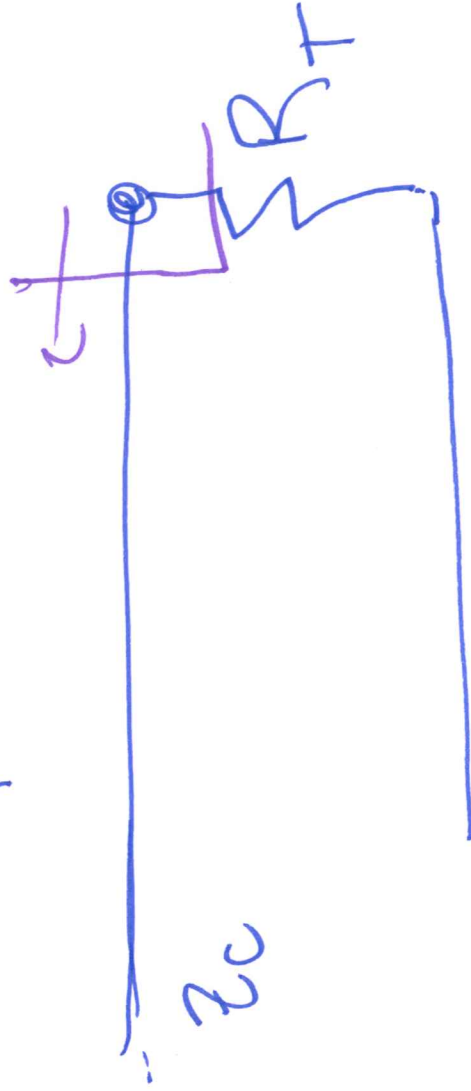


- not much  
60Hz current

$\Gamma$



$$R_T \rightarrow jX_m \parallel Z_{load}$$



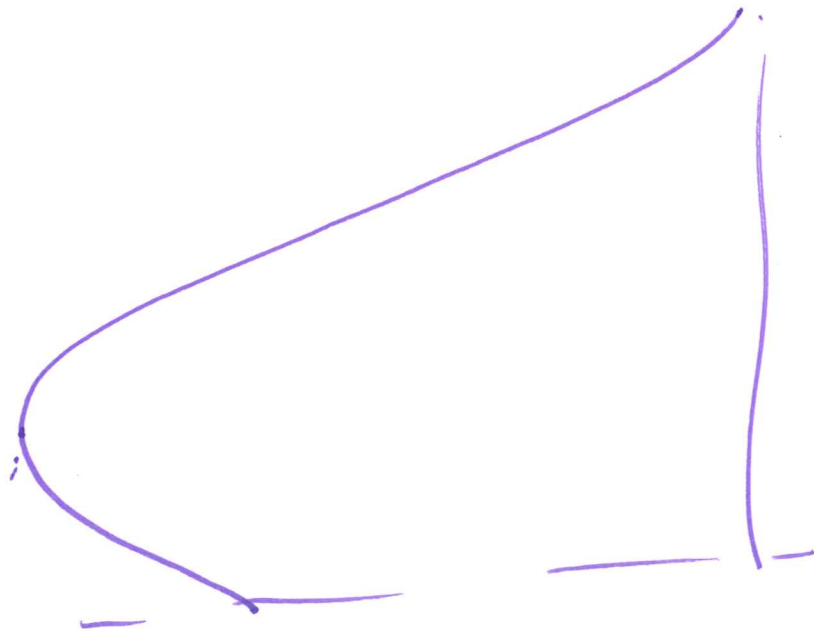
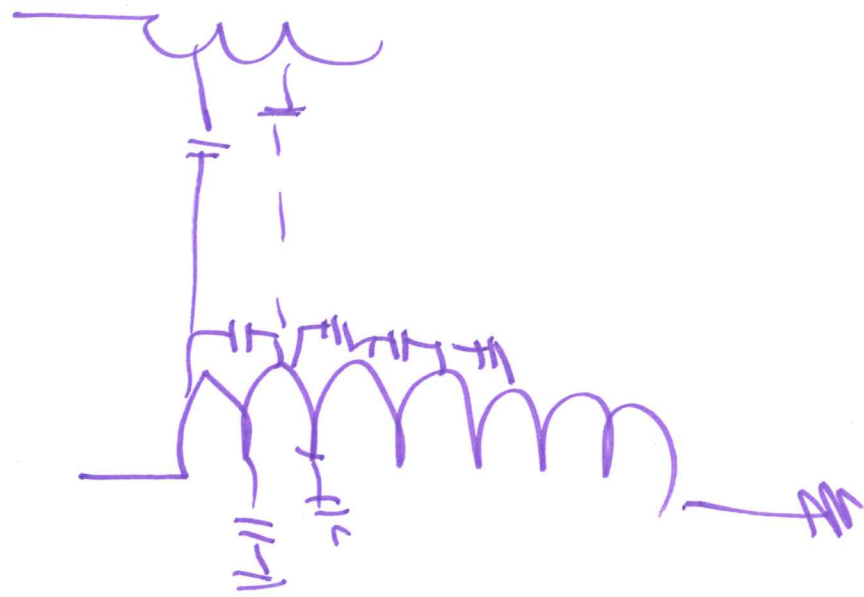
$$\Gamma_V = \frac{R_T - Z_C}{R_T + Z_C}$$

- open circuit  $R_T = \infty$

$$\Gamma_V = 1$$

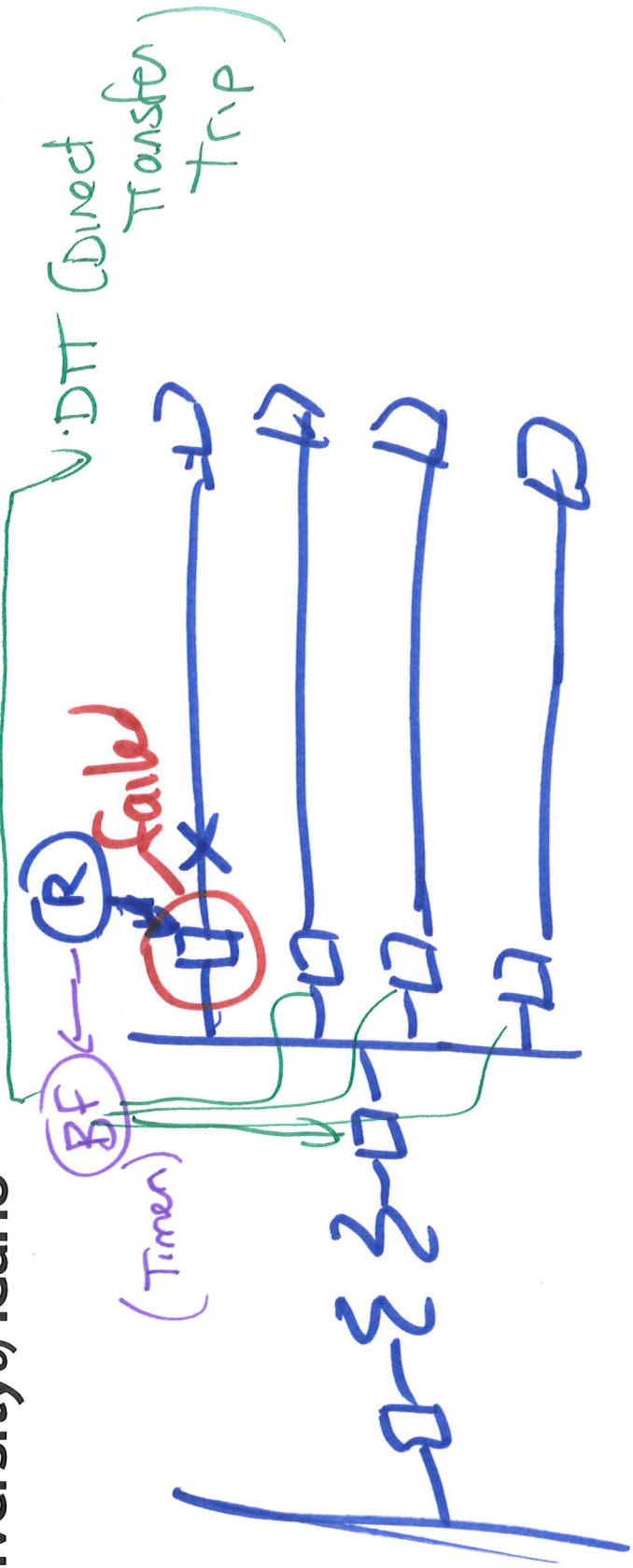
- SC  $R_T = 0$

$$\Gamma_V = -1$$



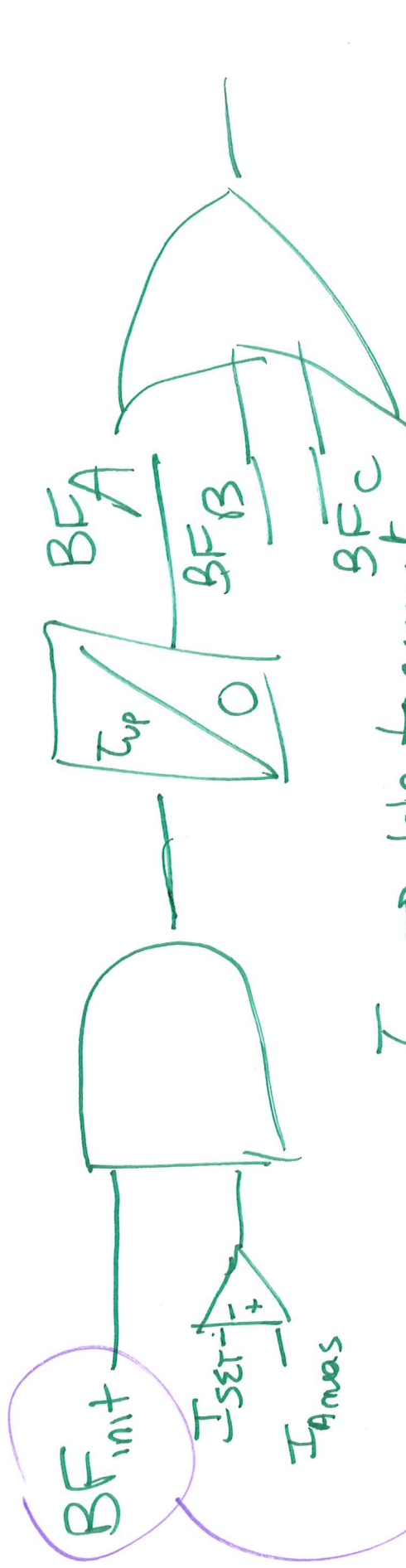
# Breaker Failure Schemes

- HISTORICALLY BF relay was a separate device
- ~~Any~~ Actuated by any relay function that generates a trip (6Z)
- BF function checks to see if CB actually tripped

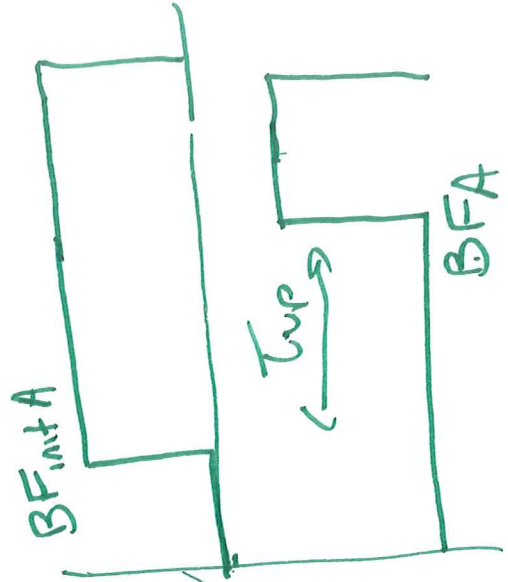


- ① - activate 2nd trip coil
- ② - If still failed - trip next devices out...

# Breaker failure logic - option 1

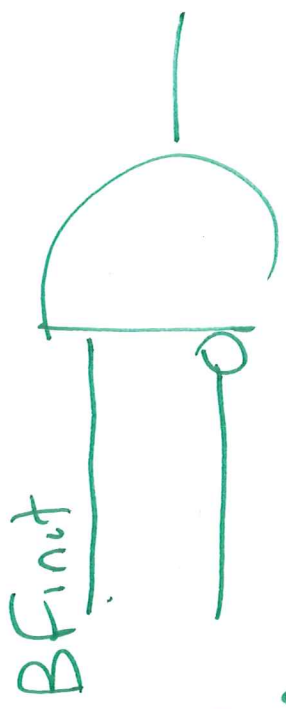


$T_{up} \rightarrow$  delay to account for breaker response + reset for fault detect



TRIP command of the ~~breaker~~ any failure TO Breaker zero in

Other inputs



oper  
Phase  
detector

2 Breaker  
contact

# Transformer Protection

- Fault protection of transformer itself
- Protecting transformer from external conditions of through faults