

## ECE 528 – Understanding Power Quality

<http://www.ece.uidaho.edu/ee/power/ECE528/>

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### Lecture 3

1

1

## Today...

- Power electronics review
- Common devices and topologies
- Some PQ issues with power electronics
- PQ Terminology

We're in PSQ ch. 2 and 5, FPQ ch 2 and 6

2

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2

## What are Power Electronics?

- Power electronics refers to devices employing semiconductors to convert and control electrical energy
- Types of voltage conversions:
  - Magnitude
  - Frequency
  - Number of phases

3

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3

## Why are power electronics important to PQ?

- The use of power electronic devices continues to increase
- They cause harmonic distortion
- The presence of harmonic distortion from power electronics can affect the power system and other loads
- Power electronic devices have particular vulnerabilities when it comes to PQ

4

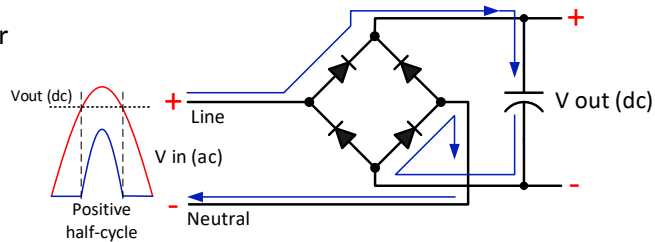
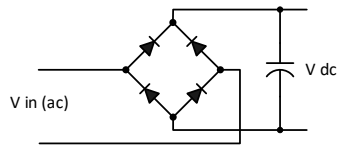
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4

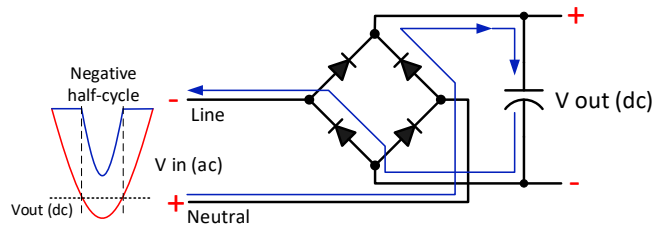
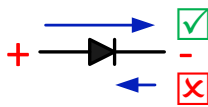
## Harmonics – from AC to DC conversion (Rectification)

*Where the current pulses come from...*

The basic rectifier with capacitor



Diodes: like one-way valves for current



Current flows on source side when  $V_{in} > V_{out}$

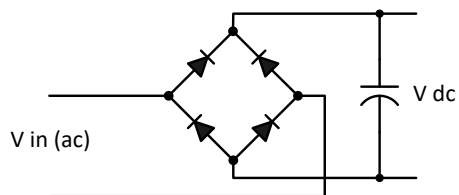
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5

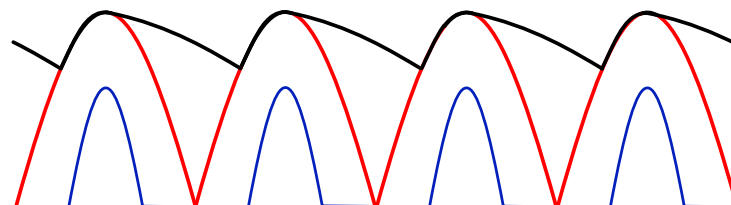
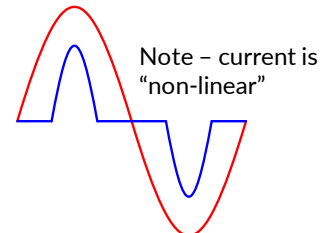
## Harmonics – from AC to DC conversion (Rectification)

The basic rectifier with capacitor



Input AC Voltage

Input AC Current



DC Voltage

Rectified Voltage

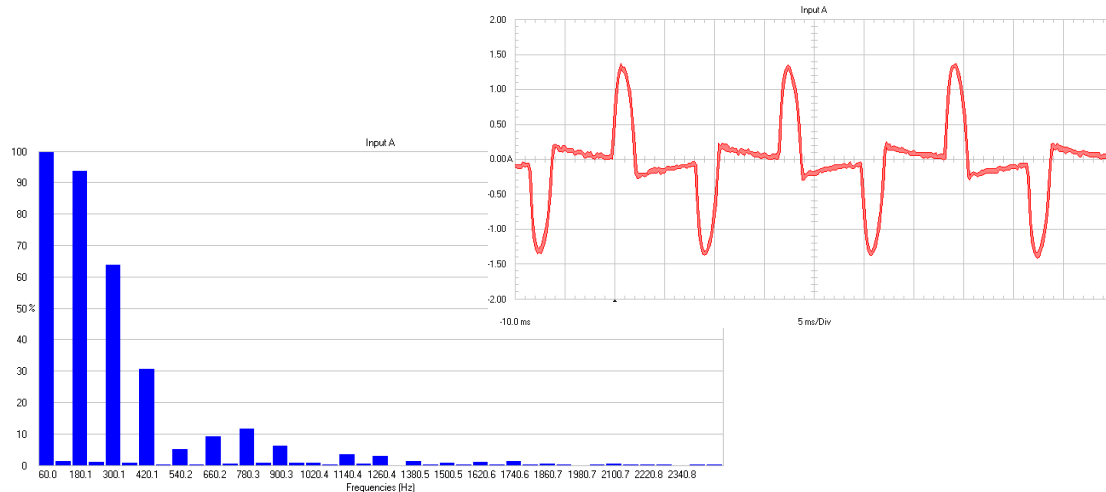
Current between rectifier and capacitor

6

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6

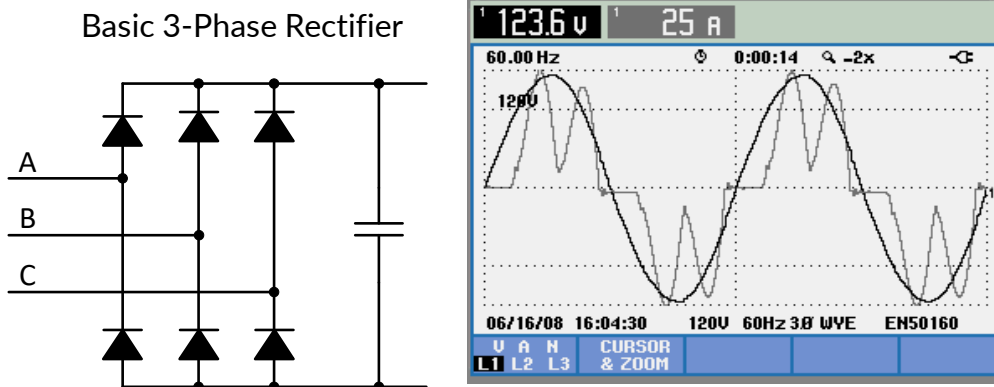
## Current for a desktop computer and its spectrum



7

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## Harmonics - 3-Phase Rectification



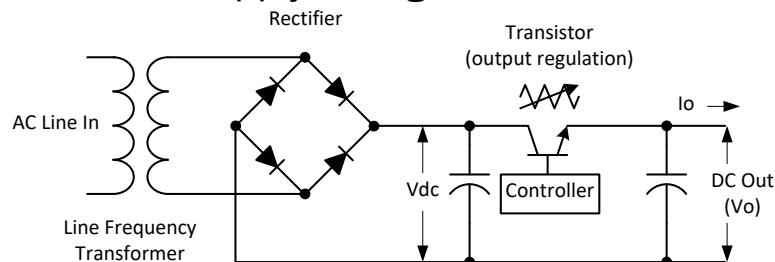
8

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## Controlling the output:

### A regulated, linear DC power supply

We want the dc output voltage to be nearly constant despite load variations and supply voltage variations.



Transistor operated in active region as a variable resistor – varies voltage drop to regulate output voltage.

9

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9

## Losses in the linear dc supply

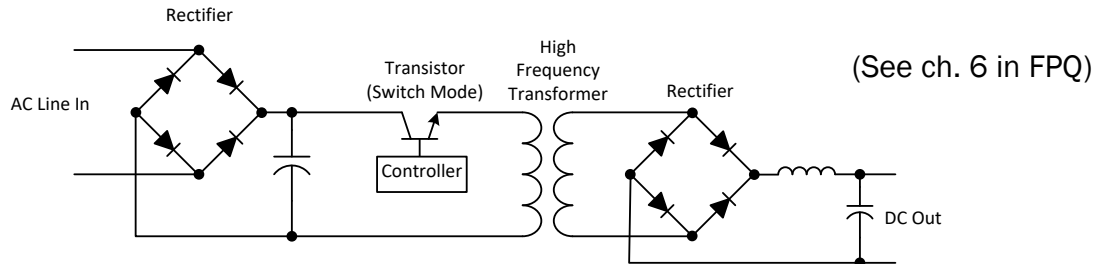
- $V_o < V_{dc}$  (output voltage < capacitor voltage)
- The power dissipated in the load is:  $V_o \cdot I_o$
- The power dissipated in the transistor is:  $P_{\text{switch}} = (V_{dc} - V_o) I_o$
- If  $V_o = \frac{1}{2} V_{dc}$ , then  $P_{\text{switch}} = \frac{1}{2} (V_{dc}) I_o$
- The “switch mode” supply reduces losses significantly.

10

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10

## Controlling the output: Switch mode power supplies



Transistor is operated as a **switch**; either on, or off.

### Advantages:

Reduced losses and weight  
More tolerant of input voltage variations

### Disadvantages

High switching frequency - interference  
Increased complexity

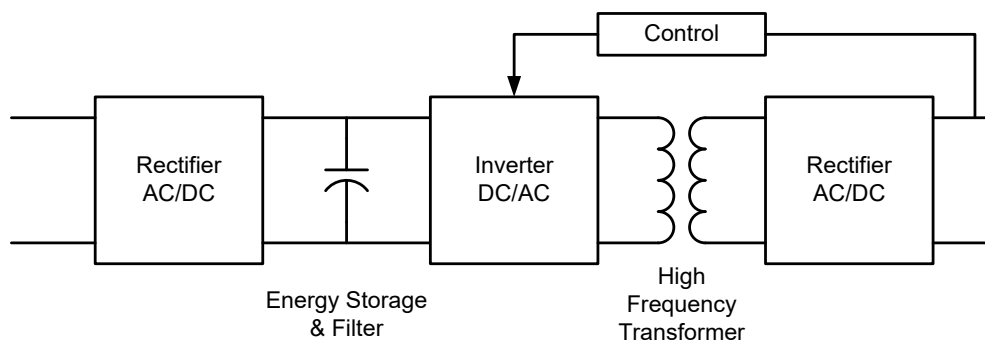
11

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11

## Topologies of converters

- The general switch mode power supply



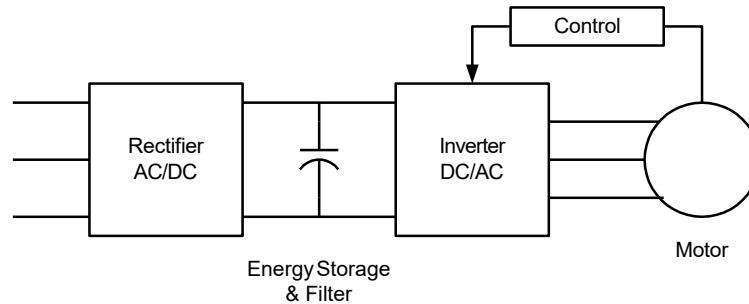
12

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12

## Topologies of converters

- The variable frequency drive (VFD, ASD, VSD)



- See PSQ pages 214-223 or FPQ pages 201-205

13

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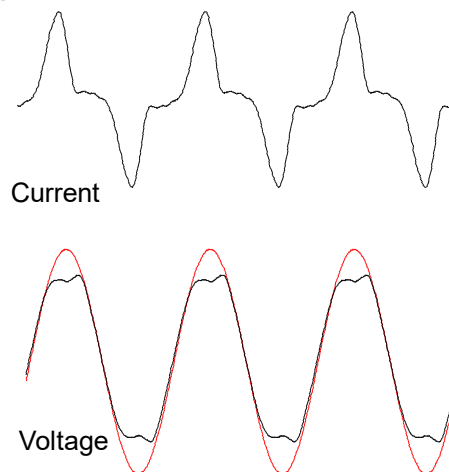
13

## Some PQ issues with power electronics

### Impact on line voltage

Distorted voltage results in distorted current, even in linear loads.

“Flat-topping” reduces DC bus voltage in power supplies, reducing ride-through time of electronic loads during voltage sags.



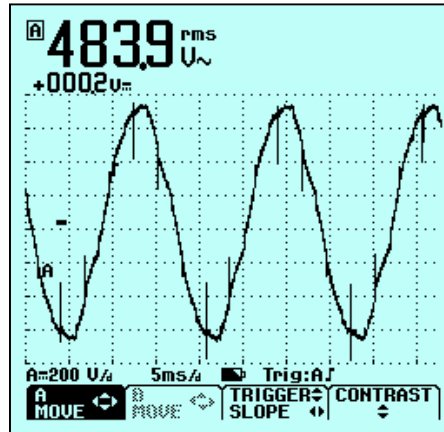
14

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14

## Some PQ issues with power electronics

- Other line-side issues
  - Higher frequencies associated with harmonics and high switching frequencies can increase capacitive coupling.
  - Line voltage notching if rectifier uses thyristors



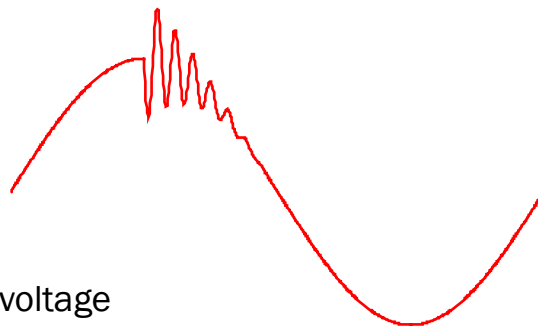
15

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15

## Power electronics vulnerabilities

- Capacitor switching.



What will happen when this voltage waveform passes through the rectifier and into the dc bus capacitor?

16

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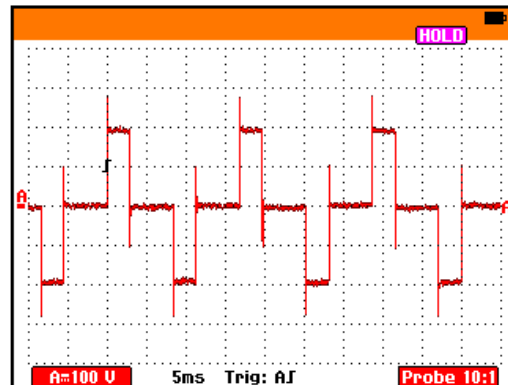
16



## Load-side issues

- Primary issue is the fact that the “ac” output from a converter contains high frequency components.

- Voltage output of a small UPS.



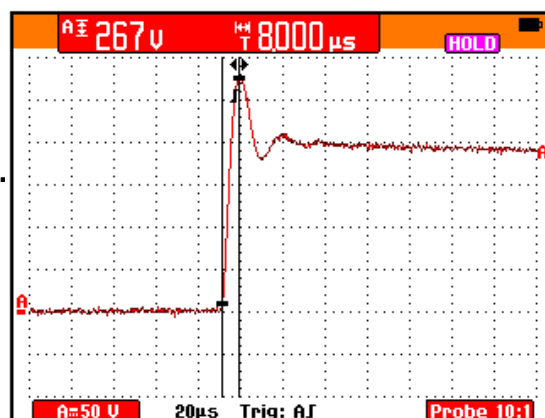
17

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17

## Load-side issues

Short rise time may affect load the same way a transient would.  
If the rise time here represents  $\frac{1}{4}$  cycle, what would the frequency be?



18

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18

## Special problem with PWM drives

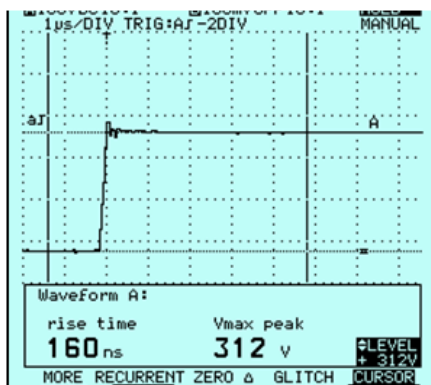
- A Pulse Width Modulated VFD can effectively vary both the voltage and frequency of its output signal.
- This is done by varying the width of output voltage pulses.
- Rise time can be 0.1 microsecond.
- If the motor leads are “long,” voltage reflections can lead to increased voltages at the motor terminals.

19

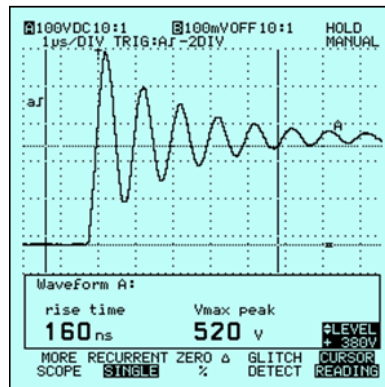
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19

## Voltage reflections in PWM drives



Normal leading edge of PWM voltage pulse



Leading edge of PWM voltage pulse with reflected voltage

20

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20

## Problems for the investigator

- Some handheld meters may give misleading readings for distorted waveforms.
- Average responding meters may read values significantly higher or lower than the True-RMS values of voltage and current, depending on the wave shape.
- Some problems, such as high frequency voltage reflections may require oscilloscopes or other more specialized monitoring equipment.

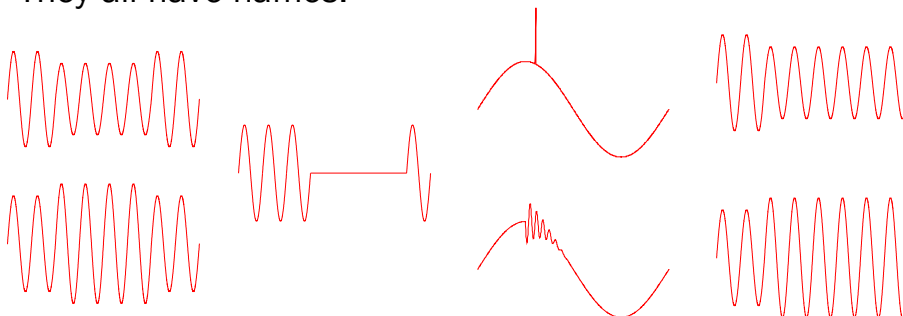
21

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21

## Terms and definitions

- Allow engineers to discuss issues, search for information, etc.
- Problem - Most of the public including engineers outside of power quality are not familiar with power quality terms and their definitions.
- They all have names:



22

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22

## Ambiguous terms

Brownout Dirty Power  
Glitch Clean Ground Spike  
Clean Power Surge  
Blip Power Bump

23

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23

## Four general types of disturbances (IEC)

- **Conducted low freq.**
  - Harmonics
  - Sags/swells/interruptions
  - Imbalance
  - DC offset
- **Conducted High freq.**
  - Transients
  - Induced high frequency signals
- **Radiated low freq.**
  - Electric and magnetic fields
- **Radiated High freq.**
  - Electric and magnetic fields

See FPQ p. 17 or PSQ p. 16

24

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24

## Categories based on duration (IEEE)

- Transients                      nanoseconds to 3 cycles
- Short duration
  - Instantaneous              0.5 – 30 cycles
  - Momentary                 30 cycles – 3 seconds
  - Temporary                 3 s – 1 minute
- Long duration                 > 1 minute
- Steady State

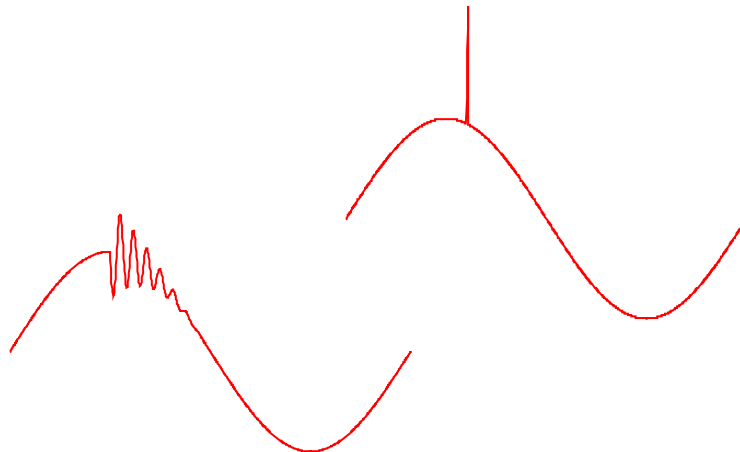
See Table 2.2 in either text

25

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25

## Transients



26

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26

## Describing transients:

- Impulsive
  - Peak magnitude
  - Time to rise/time to return to 50% of peak
  - A 1.2kV, 1.2/50ns impulsive transient
- Oscillatory
  - Frequency
  - Duration
  - Maximum absolute value
  - A 720Hz, quarter-cycle, 1.3pu oscillatory transient

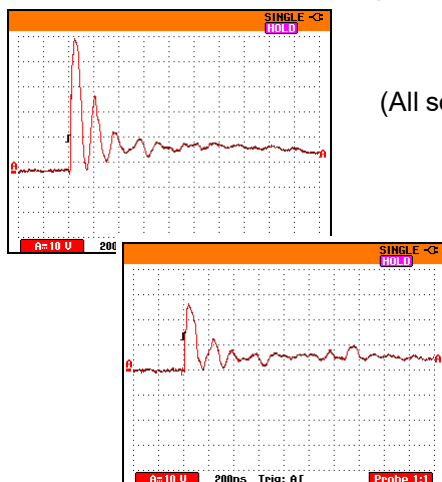
27

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27

## Events tend to have “signature” transients

### Same device switching



### Different devices switching

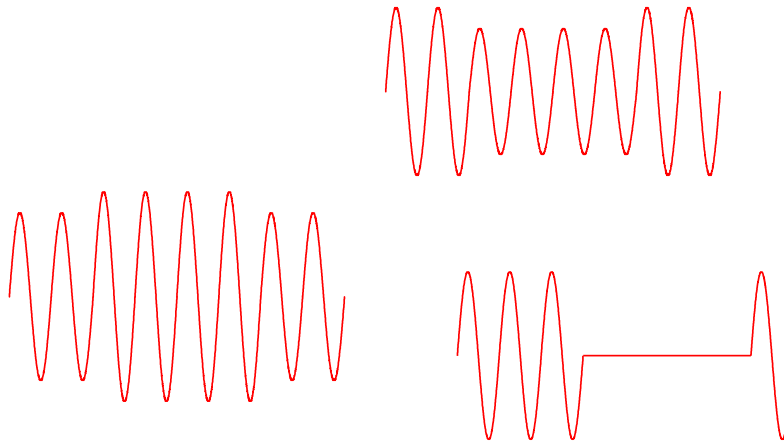


28

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28

## Short duration variations



29

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29

## Clearly describing sags, undervoltage, swells, and overvoltage

- What is “a 40% voltage sag”? Is it more or less severe than a 60% voltage sag of the same duration?
- We’ll use a remaining-voltage convention and describe sags, swells, undervoltage, and overvoltage carefully to avoid confusion.

*A sag to 40% of nominal voltage.*

30

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30

## Describing short-duration disturbances

(Include descriptors from all four columns)

Duration	phases	Disturbance	Magnitude (for sags and swells)
<ul style="list-style-type: none"> <li>Instantaneous</li> <li>Momentary</li> <li>Temporary</li> <li>Cycles</li> </ul>	<ul style="list-style-type: none"> <li>Single</li> <li>Two</li> <li>Three</li> </ul>	<ul style="list-style-type: none"> <li>Sag</li> <li>Swell</li> <li>Interruption</li> </ul>	<ul style="list-style-type: none"> <li>Percentage</li> <li>Per-unit</li> </ul>

*A momentary single-phase sag to 70% of nominal.*

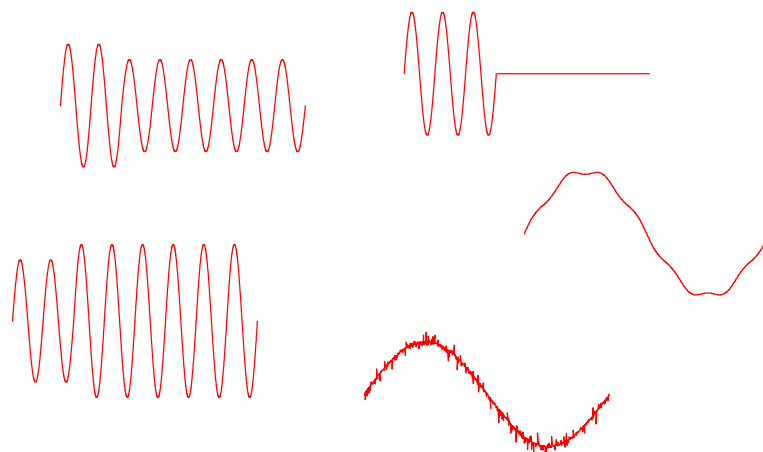
*A 4-cycle, three-phase sag to 50% of nominal.*

31

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31

## Long duration variations and steady-state conditions



32

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32



## Describing long-duration disturbances

(Include descriptors from all four columns)

Duration	phases	Disturbance	Magnitude
<ul style="list-style-type: none"> <li>Minutes and seconds</li> </ul>	<ul style="list-style-type: none"> <li>Single</li> <li>Two</li> <li>Three</li> </ul>	<ul style="list-style-type: none"> <li>Undervoltage</li> <li>Overvoltage</li> <li>Interruption</li> </ul>	<ul style="list-style-type: none"> <li>Percentage</li> <li>Per-unit</li> </ul>

*A three-minute, single-phase interruption.*

*A 2-minute, three-phase overvoltage of 1.2 per-unit.*

33

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33

## Next time...

- Read chapters 1 and 2 in FPQ and PSQ if you haven't
  - Skim both texts to familiarize yourself with them
    - Information around pages 150, 153, and 197 in FPQ may help with HW1
- Investigation planning

*Note: FPQ ch. 1 is a good review of distribution systems and apparatus.*

34

Lecture 3

34