Vdc and \(|V_a|\) controllers

- Modified Power System

- PLL

Phase Lock Loop
Controlling $V_{dc}$ (ATPDraw implementation)
- VDR and VQR

- Q and Qref
[Vac] control

(file V S C _ V D C _ a n d V A C _ c o n t r o l _ P L L . p l i 4 ; x - v a r t)
• VDR1 and VDREF

Q and QREF (controller needs some tuning)