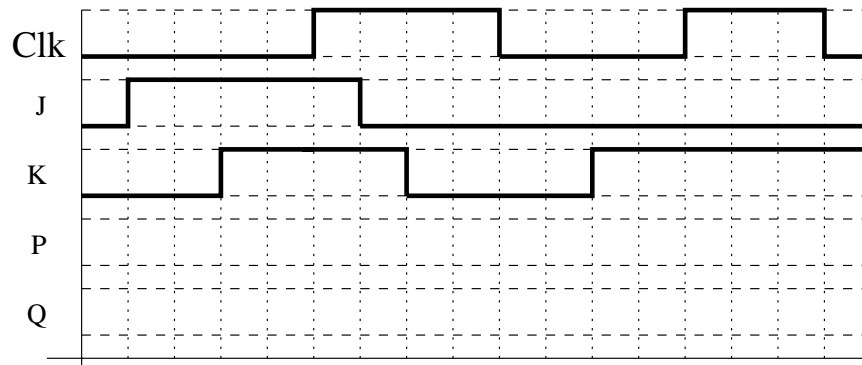




6. Complete the timing diagram for a clocked master-slave flip-flop receiving the following input waveform sequence. Designate P as the output of the first flip-flop, and Q as the output of the master-slave combination.

Use propagation delay of 0.



7. Problem 7.2 in your text
8. Problem 7.3 of your text
9. Repeat problem 7.2 in your text if the S input is now the D input to a D latch configured like the one in Figure 7-12 and the R input is the Control input.